

IN THE CLAIMS

The pending claims are as follows.

Listing of Claims

1-33. (Canceled).

34. (Currently Amended) A coding apparatus comprising:

a CRC attachment unit that attaches respective CRC-bits to a plurality of transport blocks;

a concatenating unit that concatenates the transport blocks having the CRC-bits to provide a concatenated transport block;

a code block segmentation unit that segments the concatenated transport block into code blocks to enable the code blocks to be encoded respectively; and

an error correcting coding unit that encodes each of the code blocks,

wherein said code block segmentation unit adds at least one predetermined bit to the beginning of one of the code blocks so that each of the code blocks has one of the CRC-bits as a last bit thereof and at least one of the code blocks comprises a plurality of the transport blocks.

35. (Currently Amended) A coding apparatus comprising:

a CRC attachment unit that attaches respective CRC-bits to a plurality of transport blocks;

a concatenating unit that concatenates the transport blocks having the CRC-bits to provide a concatenated transport block;

a code block segmentation unit that segments the concatenated transport block into code blocks to enable the code blocks to be encoded respectively; and

an error correcting coding unit that encodes each of the code blocks,

wherein when a number of bits of the concatenated transport block is not an integer multiple of a number of the code blocks, said code block segmentation unit adds at least one predetermined bit to the beginning of one of said code blocks so that each of the code blocks has one of the CRC-bits as a last bit thereof and at least one of the code blocks comprises a plurality of the transport blocks.

36. (Currently Amended) A coding apparatus comprising:

a CRC attachment unit that attaches respective CRC-bits to a plurality of transport blocks;

a concatenating unit that concatenates the transport blocks having the CRC-bits to provide a concatenated transport block;

a code block segmentation unit that segments the concatenated transport block into code blocks to enable the code

blocks to be encoded respectively; and

an error correcting coding unit that encodes each of the code blocks,

wherein said code block segmentation unit adds at least one predetermined bit to the beginning of one of the code blocks so as to make the code blocks the same size so that each of the code blocks has one of the CRC-bits as a last bit thereof and at least one of the code blocks comprises a plurality of the transport blocks.

37. (Previously Presented) The coding apparatus according to claim 34, wherein said at least one predetermined bit is 0.

38. (Previously Presented) The coding apparatus according to claim 35, wherein said at least one predetermined bit is 0.

39. (Previously Presented) The coding apparatus according to claim 36, wherein said at least one predetermined bit is 0.

40. (Previously Presented) A mobile station apparatus comprising the coding apparatus of claim 34.

41. (Previously Presented) A mobile station apparatus comprising the coding apparatus of claim 35.

42. (Previously Presented) A mobile station apparatus comprising the coding apparatus of claim 36.

43. (Previously Presented) A base station apparatus comprising the coding apparatus of claim 34.

44. (Previously Presented) A base station apparatus comprising the coding apparatus of claim 35.

45. (Previously Presented) A base station apparatus comprising the coding apparatus of claim 36.

46. (Currently Amended) A coding method comprising the steps of:

attaching respective CRC-bits to a plurality of transport blocks;

concatenating the transport blocks having the CRC-bits to provide a concatenated transport block;

segmenting the concatenated transport block into code blocks to enable the code blocks to be encoded respectively; and

performing error correcting coding on each of the code blocks,

wherein said segmenting step includes adding at least one predetermined bit to the beginning of one of the code blocks so

that each of the code blocks has one of the CRC-bits as a last bit thereof and at least one of the code blocks comprises a plurality of the transport blocks.

47. (Currently Amended) A coding method comprising the steps of:

attaching respective CRC-bits to a plurality of transport blocks;

concatenating the transport blocks having the CRC-bits to provide a concatenated transport block;

segmenting the concatenated transport block into code blocks to enable the code blocks to be encoded respectively; and

performing error correcting coding on each of the code blocks,

wherein when a number of bits of the concatenated transport block is not an integer multiple of a number of the code blocks, said segmenting step includes adding at least one predetermined bit to the beginning of one of the code blocks so that each of the code blocks has one of the CRC-bits as a last bit thereof and at least one of the code blocks comprises a plurality of the transport blocks.

48. (Currently Amended) A coding method comprising the steps of:

attaching respective CRC-bits to a plurality of transport blocks;

concatenating the transport blocks having the CRC-bits to provide a concatenated transport block;

segmenting the concatenated transport block into code blocks to enable the code blocks to be encoded respectively; and

performing error correcting coding on each of the code blocks,

wherein said segmenting step includes adding at least one predetermined bit to the beginning of one of the code blocks so as to make the code blocks the same size and so that each of the code blocks has one of the CRC-bits as a last bit thereof and at least one of the code blocks comprises a plurality of the transport blocks.

49. (Previously Presented) The coding method according to claim 46, wherein said at least one predetermined bit is 0.

50. (Previously Presented) The coding method according to claim 47, wherein said at least one predetermined bit is 0.

51. (Previously Presented) The coding method according to claim 48, wherein said at least one predetermined bit is 0.